

Abstract of the Disclosure:

A receiver circuit contains first and second inputs for feeding in first and second input signals, and an output generating an output signal dependent on the input signals. A  
5 detector circuit has a first and second signal detectors connected to the first and second inputs, respectively. The signal detectors compare amplitudes of the input signals in each case with a detection threshold and in each case provide a detector output signal. The signal detectors each have a  
10 control input for setting the detection threshold, and the control input of the first signal detector is coupled to an output of the second signal detector and the control input of the second signal detector is coupled to an output of the first signal detector. A signal processing circuit receives  
15 the detector output signals and generates the output signal according to the detector output signals.

REL/nt